

This listing of claims will replace all prior versions, and listing, of claims in the application.

1. (original) A circuit, comprising:
 - a processor voltage input line to receive a processor voltage signal;
 - a reference voltage output line to provide a reference voltage signal associated with a determination of when the processor voltage signal exceeds a threshold value; and
 - a curve shaping circuit to generate the reference voltage signal such that the reference voltage signal initially increases with increases in the processor voltage signal and then decreases with a further increase in the processor voltage signal.
2. (original) The circuit of claim 1, wherein the threshold value is associated with an acceptable voltage level for a processor.
3. (original) The circuit of claim 1, wherein the reference voltage signal decreases substantially with a further increase in the processor voltage signal.
4. (original) The circuit of claim 1, wherein the reference voltage signal follows the processor voltage signal up to a level associated with a transistor voltage threshold.
5. (original) The circuit of claim 1, wherein the reference voltage is clamped to a diode voltage threshold.
6. (original) The circuit of claim 1, further comprising:
 - a reference voltage output line to provide the reference voltage signal.

7. (original) The circuit of claim 1, further comprising:
a power indication circuit, comprising:
a processor voltage input line to receive the processor voltage signal,
a reference voltage input line to receive the reference voltage signal, and
a comparator circuit to generate a power indication signal based on the processor voltage signal and the reference voltage signal.

8. (original) The circuit of claim 7, further comprising:
a scaling circuit to generate a scaled processor voltage signal, wherein the comparator circuit is to generate the power indication when the scaled processor voltage signal exceeds the reference voltage signal.

9-11. (canceled)

12. (original) The circuit of claim 7, wherein the power indication circuit further comprises:

a power indication output line to provide the power indication signal.

13. (original) A circuit, comprising:
a processor voltage input line to receive a processor voltage signal;
a reference voltage output line to provide a reference voltage signal associated with a determination of when the processor voltage signal exceeds a threshold value; and
a curve shaping circuit to generate the reference voltage signal such that the reference voltage signal will exceed a scaled threshold value before stabilizing at the scaled threshold value.

14. (original) The circuit of claim 13, wherein the threshold value is associated with an acceptable voltage level for a processor.

15-16. (canceled)

17. (original) A processor, comprising:

a reference voltage circuit, comprising:

a processor voltage input line to receive a processor voltage signal,

a reference voltage output line to provide a reference voltage signal, and

a curve shaping circuit to generate the reference voltage signal such that the reference voltage signal initially increases with increases in the processor voltage signal and then decreases with a further increase in the processor voltage signal; and

a power indication circuit, comprising:

a processor voltage input line to receive the processor voltage signal,

a scaling circuit to generate a scaled processor voltage signal,

a reference voltage input line coupled to reference voltage output line of the reference voltage circuit, and

a comparator circuit to generate a power indication signal when the scaled processor voltage signal exceeds the reference voltage signal.

18. (original) The processor of claim 17, wherein the power indication signal is associated with an acceptable voltage level for the processor.

19. (original) A method, comprising:

generating a reference voltage signal such that the reference voltage signal initially increases with increases in a processor voltage signal and then decreases with a further increase in the processor voltage signal;

generating a scaled processor voltage signal based on the processor voltage signal; and
generating a power indication signal when the scaled processor voltage signal exceeds the
reference voltage signal.

20. (original) The method of claim 19, wherein the power indication signal is associated
with an acceptable voltage level for a processor.